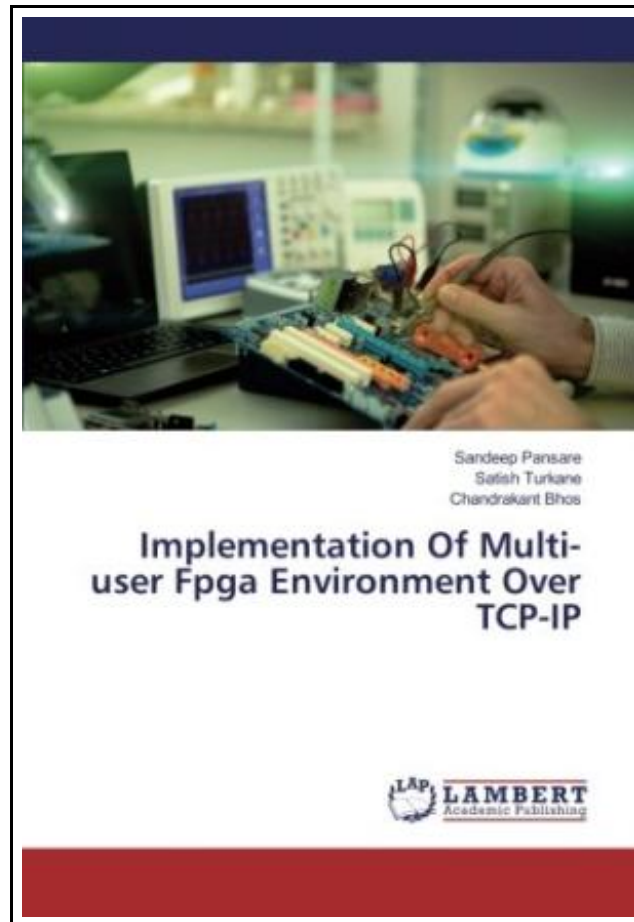


# Implementation Of Multi-user Fpga Environment Over TCP-IP



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